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Q. 4.35: Implement a full
adder with two $4 * 1$
multiplexers.*Boolean Logic*
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Q. 1.20: Convert decimal +49 and +29 to binary, using the signed-2's-complement representation and e Q.

4.30: Using a decoder and external gates, design the combinational circuit defined by the following Computer

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~~Q. 4.26: Construct a~~

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~~4 to 16 line decoder with
five 2 to 4 line decoders
with enable.~~ **Q. 4.27: A**

**combinational circuit is
specified by the following
three Boolean functions Q .**

*1.17: Perform subtraction on
the given unsigned numbers*

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*using 10's complement of the
subtrahend* ~~Binary Addition
and Subtraction With
Negative Numbers, 2's
Complements \u0026amp; Signed
Magnitude~~ **Q. 1.19: The
following decimal numbers
are shown in sign-magnitude**

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form: +9,286 and +801 Q.

~~4.31: Design a four input priority encoder with inputs as in Table 4.8, but with input D0 having Q.~~ 4.33: Construct a 16×1 multiplexer with two 8×1 and one 2×1 multiplexers. Use block

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diagrams Digital Design: Q.
1.13: Do the following
conversion problems: (a)
Convert decimal 27.315 to
binary **Q. 4.4: Design a
combinational circuit with
three inputs and one
output. (a) The output is 1**

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M. Morris Mano is an
Emeritus Professor of
Computer Engineering at the
California State University,
Los Angeles. His notable
works include the Mano

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